

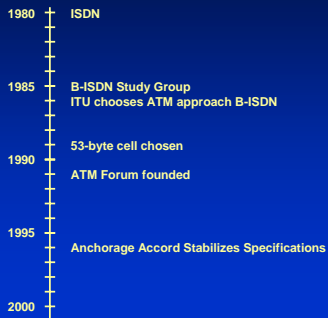
Asynchronous Transfer Mode (ATM)

- Background
- Physical and ATM Layers
- AALs
- Applications

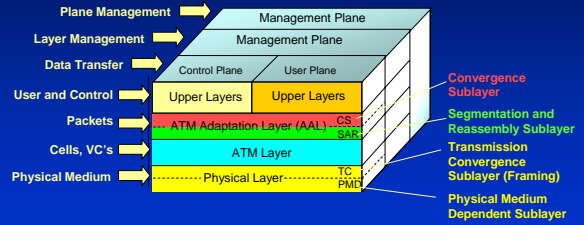
ATM Quick Highlights

- The Telecom Industry's thrust into multi-media data networking
- Comm unit is small, fixed-sized "cell" (53 bytes)
- Built to provide Quality-of-service
- Connection-oriented
- Designed to run over SONET/SDH

ATM History



The B-ISDN ATM Reference Model



Why?

- Why a small cell instead of a large packet?
 - Queue delays tend to grow as packet size grows. A small cell helps maintain streamlined flows.
 - No/little performance loss due to padding large fields
 - Small cells better for voice
 - No need for in-route fragmentation

Why?

- Why a fixed cell size instead of variable-size packets?
 - Switch architecture can be optimized to the fixed size, so switching can be done in hardware
 - Scalable parallel switch designs

Why?

- Why 53 bytes?
 - US wanted 64 payload bytes, Europe wanted 32
 - Compromised on 48
 - +5 header = 53

Why?

- Why start out with 9% Overhead?

Overhead isn't everything...

Ethernet / SS10: 9 Mb/s BW, 900 µsecs ovhd
ATM Synoptics: 78 Mb/s BW, 1,250 µsecs ovhd.
NFS trace over 1 week: 95% msgs < 200 bytes

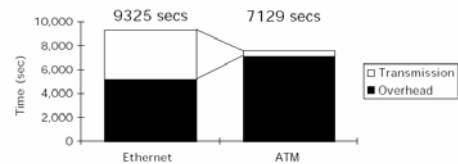


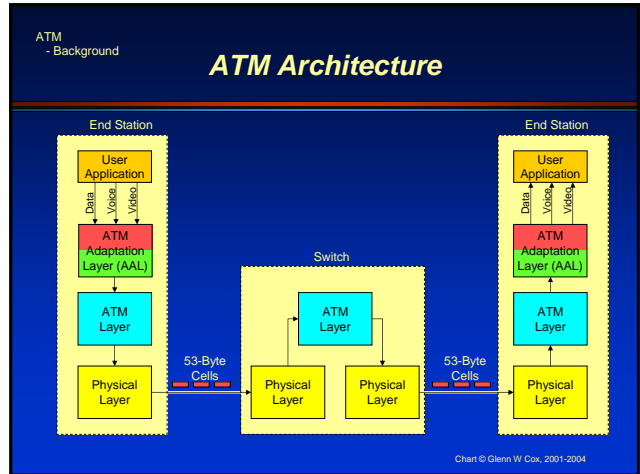
Figure from GMU Test Run 1997 <http://www-2.cs.umu.edu/fac/cs.umu.edu/academic/olaw/15347-689/public/lectures/lec24.pdf>

ATM
- Background

ATM + and -

- +
- QoS
- Multimedia Support
- Hardware Switching -> High Speed
- Connection-Oriented (-?)
-
- IP Support
- LAN arena dominated by huge installed Ethernet base
- Ethernet growing toward MAN, WAN
- Connection-Oriented (+?)
- Living up to the hype of the early 90's

Chart © Glenn W Cox, 2001-2004

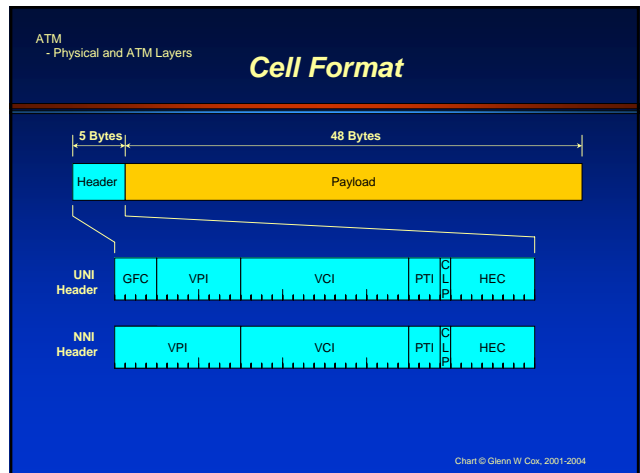


ATM
- Physical and ATM Layers

ATM

- Background
- Physical and ATM Layers
 - Cells, Formats, and Addressing
 - Virtual Circuits
 - Switches and Media
 - Interfaces
- AALs
- Applications

Chart © Glenn W Cox, 2001-2004



ATM
- Physical and ATM Layers

Header Error Control (HEC)

HEC covers the header only, not the payload -- the goal is to ensure correct delivery

First Four Cell Header Bytes

$x^8 + x^2 + x + 1$ → Remainder + 01010101 HEC

If P(bit error) = 10^{-8} , then P(undetected header error) = $\sim 10^{-20}$:
at OC-3, about 1 per 90,000 yrs

HEC also assists in synchronizing:

- Look at 53-byte sequences until you find one where the HEC field works correctly
- If this holds up for D sequences in a row, assume you've synched

- p(bad synch) = $\sim 2^{-40}$

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ATM
- Physical and ATM Layers

UNI Header Fields

GFC - General Flow Control
Only used between host and network. Overwritten by first switch.

VPI - Virtual Path ID

VCI - Virtual Circuit ID

PTI - Payload type ID

CLP - Cell Priority (ID's cells for deletion when congestion experienced)

HEC - Header Checksum (all 1-bit errors corrected, 90% of multi-bit errors detected)

Chart © Glenn W Cox, 2001-2004

ATM
- Physical and ATM Layers

NNI Header Fields

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Chart © Glenn W Cox, 2001-2004

ATM
- Physical and ATM Layers

PTI Field Codes

PTI	Meaning
000	User Data Cell Type 0 No congestion experienced
001	User Data Cell Type 1 No congestion experienced
010	User Data Cell Type 0 Congestion experienced
011	User Data Cell Type 1 Congestion experienced
100	Maintenance info between adjacent switches
101	Maintenance info between source and destination switches
110	Resource management cell (for ABR congestion control)
111	Reserved

Used by AAL5 to denote end of message

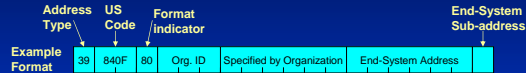
Explicit Forward Congestion Indicator (EFCI)
Set by Congested switch

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ATM Addressing

E.164: Telephone Numbers - up to 15 digits

ATM End-System Addresses (AESAs) -- 20-byte Addresses



Address Aggregation (Hierarchical Routing)

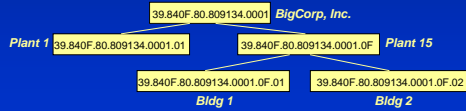


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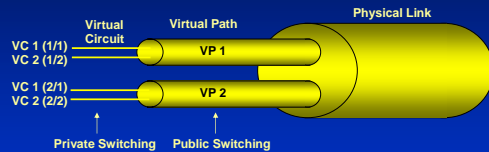
ATM

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Virtual Paths, Virtual Circuits

A Link can Carry Multiple Virtual Paths,
which can Carry Multiple Virtual Circuits



Advantage of Two-Level Approach:
A Bundle of VC's can be switched by switching a single VP

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Virtual Circuits

- Normally Unicast, but one-way Multicasting Supported
- Unidirectional, but a pair can be created with same ID -- effectively full-duplex
- Customers can lease a VP, then allocate VC's within it ("Permanent VP")
- Types of VC's:
 - Standard VC ("PVC") -- Static route
 - Soft VC -- Route can be changed in event of failure
 - Signalled VC ("SVC") -- Demand connection initiated by used

Chart © Glenn W Cox, 2001-2004

VC Connection Messages

Message	Meaning (if from host)	Meaning (if from Network)
SETUP	Call request	Incoming call
CALL PROCEEDING	ACK Incoming call	ACK Call request
CONNECT	Incoming call accepted	Call request accepted
CONNECT ACK	ACK Call request accepted	ACK Incoming call accepted
RELEASE	Terminate request	Terminate req from remote
RELEASE COMPLETE	ACK Terminate from remote	ACK terminate request

Chart © Glenn W Cox, 2001-2004

Virtual Circuit Setup Process

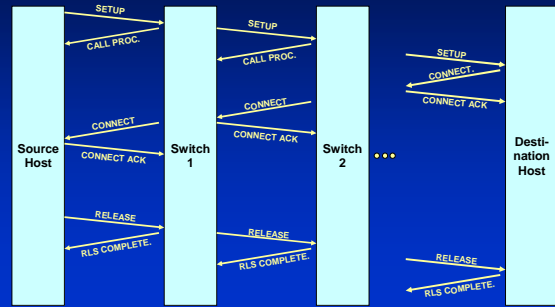


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ATM Broadcasting /Multicasting

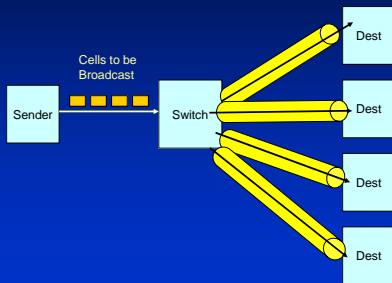


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Operation Administration and Maintenance (OA&M)

Supervision, Maintenance, Testing, Performance Measurement, Loopbacks
Organized into levels according to Network Segment Type

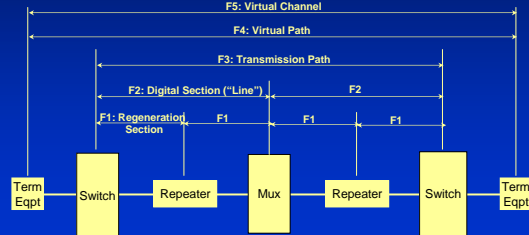


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Preassigned VPI and VCI Numbers

VPI	VCI	Meaning
0	0	Unassigned
0	1	Metasignaling
0	3	F4 Flow (segment)
0	4	F4 Flow (end-to-end)
0	5	Signaling
0	15	SMDS
0	16	Intermediate Layer Management Interface (ILMI)

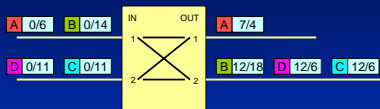
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Switch Operation



Switching Table

IN		OUT	
Port	ID	Port	ID
1	0/6	1	7/4
1	0/14	2	12/18
2	0/11	2	12/6

Label
Switching

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Transmission Media

- Baseline is SONET (B-ISDN)
- Short runs, Cat 5 TP OK
- All runs point-to-point

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ATM
- Physical and ATM Layers

SONET / SDH

Bell: Synchronous Optical Network (SONET)
CCIT: Synchronous Digital Hierarchy (SDH) } Only minor differences

9 Rows | 125 usec

90 Columns

- Synchronous, frame-oriented, TDM
- For basic SONET:
 - 6480 bits/125usec = 51.84 Mbps total ("STS-1")
 - User data rate -- 50Mbps

Chart © Glenn W Cox, 2001-2004

ATM
- Physical and ATM Layers

SONET Multiplexing

Chart © Glenn W Cox, 2001-2004

ATM
- Physical and ATM Layers

SONET/SDH Data Rates

SONET		SDH	Data Rate	
Electrical	Optical	Optical	Gross	User
STS-1	OC-1		51.84	49.5
STS-3	OC-3	STM-1	155.52	148.6
STS-9	OC-9	STM-3	466.56	445.8
STS-12	OC-12	STM-4	622.08	594.4
STS-18	OC-18	STM-6	933.12	891.6
STS-24	OC-24	STM-8	1244.16	1188.9
STS-36	OC-36	STM-12	1866.24	1783.3
STS-48	OC-48	STM-16	2488.32	2377.7

STS-1 Mux'ed

"OC-x" means multiple users, muxed
"OC-xc" means one user - slightly higher User B/W

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ATM
- Physical and ATM Layers

ATM over SONET

- ATM designed to run over SONET OC-3c
- Basic: 155.52 Mbps gross rate
 - Usually quoted as 155 Mbps
- New generation runs at OC-12 (622 Mbps), OC-48 (2.4 Gbps)

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ATM Interfaces

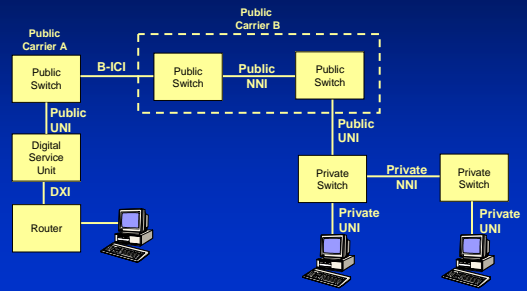


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Broadband Inter-Carrier Interface (B-ICI)

- Public Network-to-Network Interface
- Based on Broadband ISDN User-Part (B-ISUP) messages

Chart © Glenn W Cox, 2001-2004

NNI

- Switch-to-switch interface protocol
- Two versions: Public and private (similar, more flexibility in private version)
- NNI Includes:
 - Routing protocol (Link-state/OSPF)
 - Signaling protocol for link setup/teardown

Chart © Glenn W Cox, 2001-2004

ATM
- Physical and ATM Layers

UNI

- Protocol for interfacing with user equipment
- Follows ITU-T Q.2931 message format

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ATM
- Physical and ATM Layers

DXI

"Frames In, Cells Out"

Provides Frame-Based Access to an ATM Network

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ATM
- Physical and ATM Layers

DXI Variants

Mode	AALs Supported	VCs Supported
1a	AAL5	1023
1b	AAL5	1023
	AAL3/4	1
2	AAL3/4,5	16.6M

1	2	0-9232	2	1
Flag	Header	Body	FCS	Flag

1	2	0-9224	2	1
Flag	Header	Body	FCS	Flag

1	2	0-65535	4	1
Flag	Header	Body	FCS	Flag

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ATM
- AAL Layer

ATM

- Background
- Physical and ATM Layers
- AALs
 - AAL Overview
 - AAL 1 and 2
 - AAL 3/4
 - AAL 5
- Applications

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ATM
- AAL Layer

Why AAL?

An "impedance matcher"
between ATM and
higher-level protocols
with variable-length cells

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ATM
- AAL Layer

The Evolution Of the AAL's

Original Service Classes

Telecom Industry	A	Real time Constant Bit Rate Connection-Oriented	→ AAL1 →
	B	Real-time Variable Bit Rate Connection-Oriented	→ AAL2 →
	C	Non-real-time Variable Bit Rate Connection Oriented	→ AAL3 → AAL3/4 →
	D	Non-real-time Variable Bit Rate Connectionless	→ AAL4 →

Computer
Industry → "Simple
Efficient
Adaptation
Layer" → AAL5 →

Chart © Glenn W Cox, 2001-2004

ATM
- AAL Layer

Cell Formation

The diagram illustrates the process of cell formation. It starts with an **Application** layer sending data to the **AAL Layer**. The AAL Layer consists of a **Convergence Sublayer** and a **SAR Sublayer**. The **Original Message** (Data) is first processed by the Convergence Sublayer to create a **CS PDU** (Convergence Sublayer Protocol Data Unit), which consists of a **CS** header and the **Data**. This CS PDU is then processed by the SAR Sublayer to create **SAR PDUs** (Segmentation and Reassembly Protocol Data Units). The SAR PDU is split into **Data part 1** and **Data part 2**, each with its own **CS** header and **TR** (Trailer) field. Finally, the ATM Layer takes these SAR PDUs and creates **ATM CELLS**. Each ATM Cell contains an **ATM** header, a **CS** header, **Data part 1** or **Data part 2**, and a **TR** trailer.

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ATM
- AAL Layer

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AAL 1

- Designed to support Class A traffic (voice)
- Voice has good error tolerance -> No bit error control (CRC) needed
- Sequence numbers needed to ID missing cells

AAL 1 Convergence Sublayer

- Detects lost cells
- Detects mis-delivered ("misinserted") cells
- Smooths incoming traffic to minimize jitter
- Breaks bit stream into 47/46-byte segments for SAR sublayer
- Does not add headers or trailers

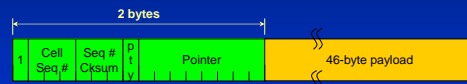
AAL 1 SAR PDU (non-pointer type)

- Adds sequence # with protection (checksum)
- Adds parity bit (even) over header



AAL 1 SAR PDU (Pointer Type)

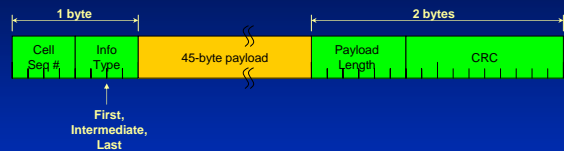
- Pointer field gives offset to start of next message (0-92 bytes)



AAL 2

- Designed to support Variable Bit Rate ("Bandwidth on Demand")
- Provides for partial payloads to support low-rate data with low latency
- Error protection over full PDU
- Simple flag to indicate position in message

AAL2 SAR PDU

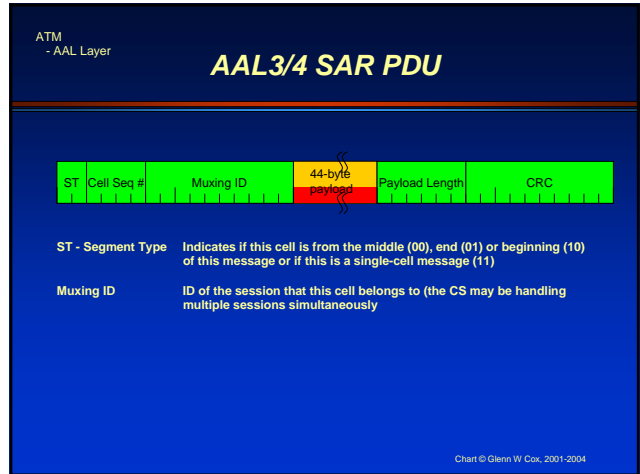
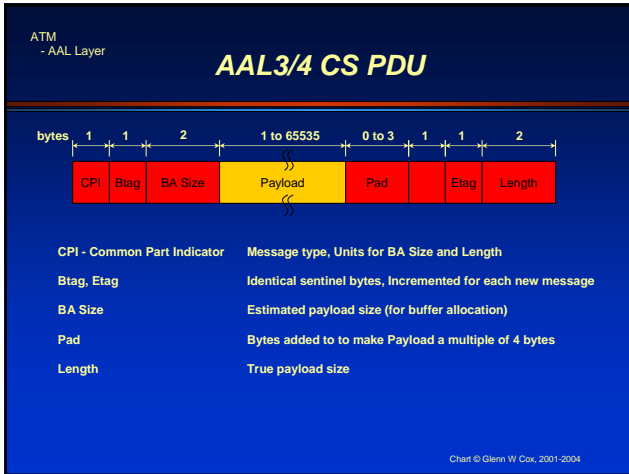


ATM

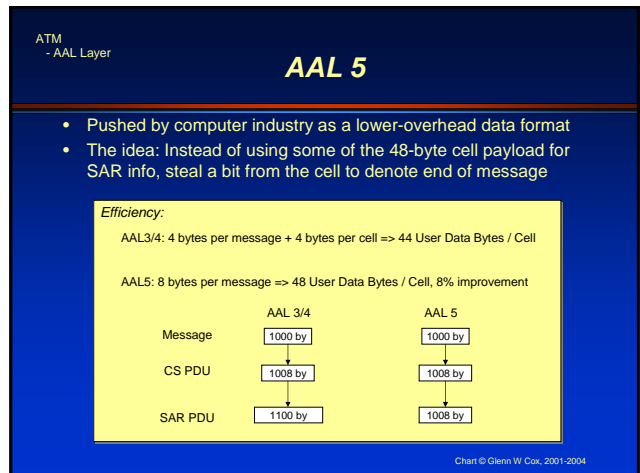
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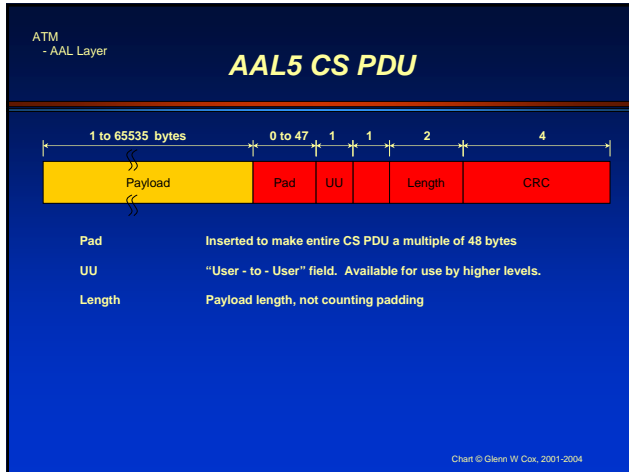
AAL 3/4

- Originally 2 separate AALs:
 - AAL3: Connection-oriented packet svcs (X.25)
 - AAL4: Connectionless svcs (IP)
- Eventually combined into a single type for all data service
- Data support overtaken by AAL5



- ATM
- AAL Layer
- ## ATM
- Background
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 - AAL Overview
 - AAL 1 and 2
 - AAL 3/4
 - **AAL 5**
 - Applications
- Chart © Glenn W Cox, 2001-2004





- ATM
- AAL Layer
- ## AAL5 SAR
- Simply breaks CS PDU into 48-byte chunks and passes them to ATM Layer.
 - No overhead bytes added.
- Chart © Glenn W Cox, 2001-2004

- ATM
- Applications
- ## ATM
- Background
 - Physical and ATM Layers
 - AALs
 - Applications
 - QoS
 - LANE
 - IP over ATM, MPOA
- Chart © Glenn W Cox, 2001-2004

- ATM
- Applications
- ## ATM QoS
- A (The?) Major ATM selling point vs. Best-Effort
 - The idea:
 - At VC setup, sender specifies level and quality of service required, also planned traffic profile.
 - While establishing VC, network attempts to allocate resources to meet requirements.
 - Requirements are agreed to or available capability is passed back to sender.
 - During transmission, network enforces traffic profile
- Chart © Glenn W Cox, 2001-2004

ATM Classes of Service

Class	Description	CLR - Cell Loss Ratio	CTD - Cell Transfer delay	CDV - Cell Delay Variation	PCR - Peak Cell Rate	SCR - Sustained Cell Rate	BT - Burst Tolerance	Flow Control
CBR	Constant bit rate (e.g., Phone traffic)	Y	Y	Y	Y			
VBR-RT	Variable bit rate, Real-Time (e.g., Interactive Compressed Video)	Y	Y	Y	Y	Y	Y	
VBR-NRT	Variable bit rate, Non-Real-Time (e.g. Multimedia email)	Y		Y	Y	Y	Y	
ABR	Available bit rate (e.g., File Xfer, email)	Y						Y
UBR	Unspecified bit rate (e.g., TCP/IP)				Y			

Chart © Glenn W Cox, 2001-2004

QoS Parameters

Parameter	Meaning
PCR - Peak Cell Rate	Max rate req'd
SCR - Sustained Cell Rate	Avg rate req'd
MCR - Minimum Cell Rate	Min acceptable rate (Used in ABR Service)
CDVT - Cell delay variation tolerance	Max acceptable jitter
CLR - Cell Loss Ratio	Fraction of cells lost or late
CTD - Cell Transfer Delay	Delivery time (mean and max)
CDV - Cell Delay Variation	Measured jitter
CER - Cell Error Rate	Fraction with one or more errors
SECBR - Severely-Errored Cell Block Ratio	Fraction of "M"-cell blocks with "N" or more errored cells
CMR - Cell Misinsertion Rate	Fraction delivered to wrong destination
BT - Burst Tolerance	Max Burst That can be sent at Peak Rate

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Traffic Shaping

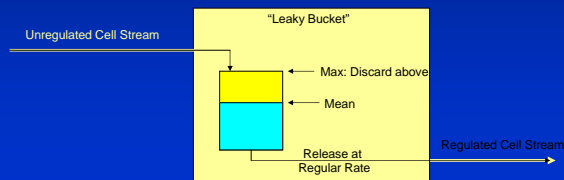
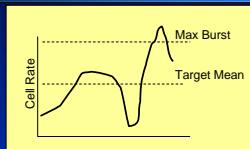
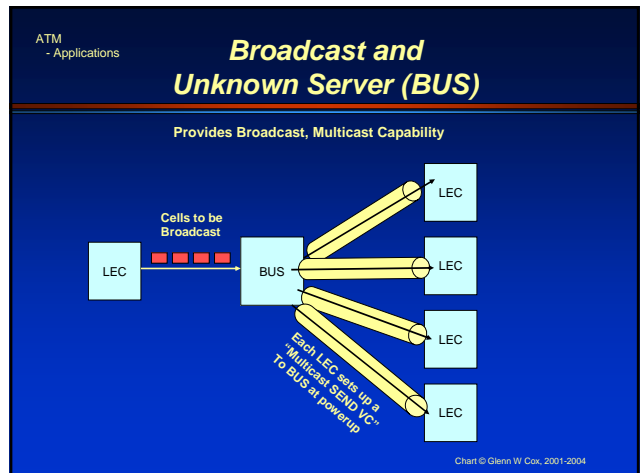
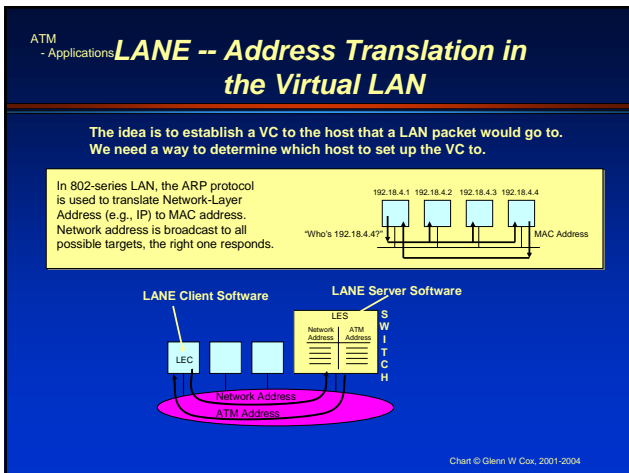
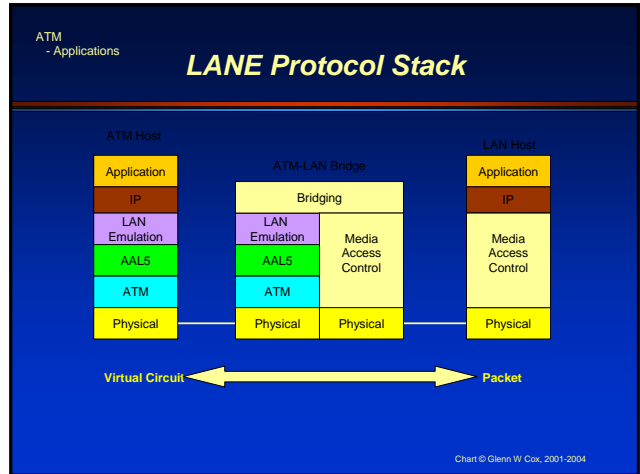
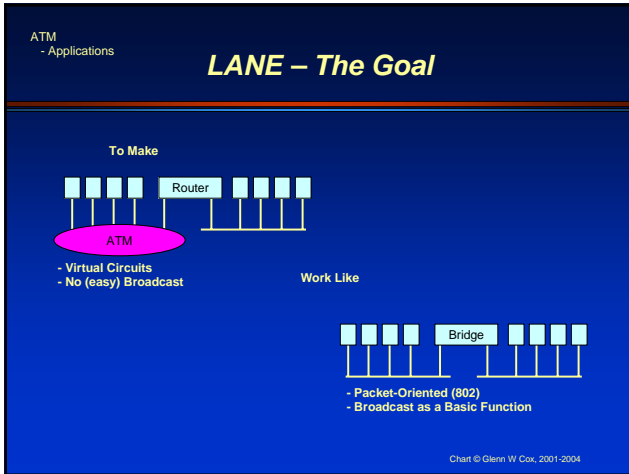


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Agenda

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- AALs
- Applications
 - QoS
 - LANE
 - IP over ATM, MPOA

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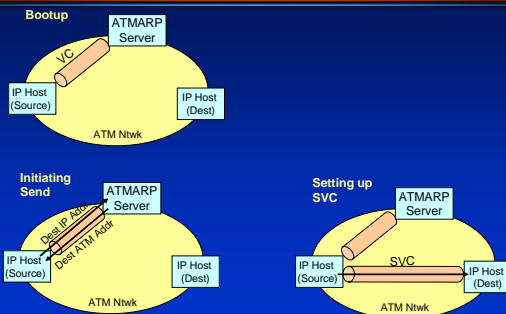
ATM

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Classical IP over ATM

- Could use LANE to implement ATM-IP interoperability, but may be too much overhead for large networks
- Defined by IETF (RFC 1577)
- The key is IP <-> ATM address resolution
 - ATM Address Resolution Protocol (ATMARP)
 - Inverse ATMARP (InATMARP)

IP over ATM Connection Setup Process



MultiProtocol Over ATM (MPOA)

LANE supports a single legacy LAN format over ATM. MPOA extends this to multiple LAN types